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Docket No.: L&L-I0232

Date: January 16, 2002

U.S. Patent and Trademark Office P.O. Box 2327

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Enclosed herewith are the necessary papers for filing the following application for Letters Patent:

Applicant

WOLFGANG RÖSNER ET AL.

Title

METHOD FOR PRODUCING A VERTICAL SEMICONDUCTOR

TRANSISTOR COMPONENT AND A VERTICAL SEMICONDUCTOR

TRANSISTOR COMPONENT

5 sheets of formal drawings.

The payment in the amount of \$ 740.00 covering the filing fee.

Information Disclosure Statement and 3 References.

PCT Cover Sheet WO 01/06542 A2 and WO 01/06542 A3.

This application is being filed without a signed oath or declaration under the provisions of 37 CFR 1.53(f). Applicants await notification of the date by which the oath or declaration and the surcharge are due, pursuant to this rule.

The Patent and Trademark Office is hereby given authority to charge Deposit Account No. 12-1099 of Lerner and Greenberg, P.A. for any fees due or deficiencies of payments made for any purpose during the pendency of the above-identified application.

Respectfully มbmitted

Spólicants

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